

LOAD PORT OF A SEMICONDUCTOR MANUFACTURING APPARATUS
HAVING INTEGRATED KINEMATIC COUPLING PINS AND SENSORS, AND
METHOD OF LOADING WAFERS USING THE SAME

BACKGROUND OF THE INVENTION

1. Field of the invention

[0005] The present invention relates to a semiconductor manufacturing apparatus. More particularly, the present invention relates to a load port of a semiconductor manufacturing apparatus.

2. Description of the Related Art

[0010] Fig. 1 illustrates a typical loading section 100 of a semiconductor manufacturing apparatus at which disk-shaped semiconductor wafers are loaded into the apparatus. The loading section 100 includes a load port (200 in Fig. 2), a mini-chamber 110 having a wafer transfer robot 112 and a door opening robot (not shown), and a load lock chamber 120. A wafer cassette 130 that stores disk-shaped wafers is safely placed by hand or by an automated material handling system (AMHS) onto the load port 200 outside the mini-chamber 110. A front opening unified pod (FOUP) is widely used as the wafer cassette 130 for wafers that are 300 mm in diameter.

[0015] Subsequently, the door opening robot of the mini-chamber 110 opens a door 132 of the wafer cassette 130. Then the wafer transfer robot 112 transfers the wafers stacked in the cassette 130 into the load lock chamber 110. The wafers are transferred from the load lock chamber 120 to a process chamber (not shown).

[0020] Referring now to Fig. 2, the load port 200 onto which the FOUP 130 is placed includes a plurality of kinematic coupling pins 220 and a plurality of sensors 240 arrayed on a flat surface thereof. The FOUP 130 includes grooves (not shown) that receive the kinematic coupling pins 220 when the FOUP 130 is placed on the load port 200. The sensors 240 contact a portion of a bottom surface of the FOUP 130 to sense whether the FOUP 130 is properly resting on the load port 200.

[0025] If the sensors 240 do not sense that the FOUP 130 is positioned properly on the load port 200, a subsequent process is not executed. That is, the process in which the door opening robot opens the door 132 of the FOUP 130 and the wafer transfer robot 112 transfers wafers from the FOUP 130 into the load lock chamber 120, will not be carried out.

[0030] In the load port 200 shown in Fig. 2, the kinematic coupling pins 220 substantially contact the FOUP 130 to support it and the sensors 240 only sense whether the FOUP 130 is properly resting on the load port 200. Therefore, if a foreign object or substance lies on the sensors 240, the sensors

240 can incorrectly sense that the FOUP 130 is properly situated on the load port 200. In addition, the sensors 240 are not as tall as the kinematic coupling pins 220. Therefore, even when the FOUP 130 is resting properly on the kinematic coupling pins 220, a bottom surface of the FOUP 130 may not sufficiently contact the sensors 240. In this case, the sensor 240 will not perform properly, i.e., will not correctly sense the presence of the FOUP 130.

[0035] Still further, the load port 200 can not determine whether wafers are stored in the wafer cassette. An empty wafer cassette 130 can sometimes be erroneously transferred onto the load port 200. In this case, even though the wafer cassette 130 is properly placed on the load port 200, a subsequent process is initiated. Accordingly, a processing error occurs.

SUMMARY OF THE INVENTION

[0040] An object of the present invention is to overcome the problems described above.

[0045] More specifically, one object of the present invention is to provide a load port that can accurately sense whether a wafer cassette has been properly placed thereon in preparation for the loading of wafers into a chamber of a manufacturing apparatus.

[0050] Another object of the present invention is to provide a load port which can discriminate whether a wafer cassette placed thereon contains any

semiconductor wafers.

[0055] In order to achieve the above objects, the load port of the present invention includes a support member, a plurality of kinematic coupling pins projecting upwardly from the support member, and a plurality of sensors integrated with the coupling pins.

[0060] The sensors are operable to sense for the presence of the bottom of the cassette and thereby determine whether the wafer cassette is resting properly on the load port. The contact of each sensor, except at an upper end thereof, is embedded in a respective kinematic coupling pin. The upper end of the sensor protrudes from the kinematic coupling pin. Preferably, at least three kinematic coupling pins and corresponding sensors are arrayed on the support member so as to balance a wafer cassette that is placed properly thereon. The sensors may be photo sensors or on-off sensors (switches). Alternatively, the sensors may be weight-detecting sensors that make use of piezoelectric elements. The wafer cassette has grooves in the bottom surface thereof, and the grooves receive the kinematic coupling pins and the sensors when the wafer cassette properly rests on the load port.

[0065] According to the present invention, it is possible to accurately sense whether the wafer cassette is situated properly on the support member of the load port and/or to discriminate whether the wafer cassette contains any wafers, because the sensors are integrate with the kinematic coupling pins

which support the cassette directly. Thus, the reliability of the semiconductor manufacturing process is enhanced.

[0070] Another object of the present invention is to provide a method of loading wafers into a chamber of a semiconductor apparatus that prevents processing errors from occurring.

[0075] To achieve this object, the method of the present invention entails detecting for the presence of the bottom of the cassette at a plurality of sites located on the upper surface of the support member, and measuring the load exerted by the cassette. If the bottom of the cassette is detected as being present at each of the detection sites, then the cassette is determined to be resting properly for the transfer of wafers therefrom. And, if the load exceeds a predetermined value corresponding to the weight of a cassette, then it is determined that the cassette contains a wafer(s). A control signal is issued to initiate the manufacturing process only once these conditions are established is. In particular, the door of the cassette is opened, and a robot is commanded to transfer wafers from the cassette into a chamber of the manufacturing apparatus.

[0080] The manufacturing process is thus never initiated when the cassette is situated improperly on the load port or not at all, and/or when the cassette is situated properly but does not contain wafers.

BRIEF DESCRIPTION OF THE DRAWINGS

[0085] These and other objects, features and advantages of the present invention will be better understood from the following detailed description of the preferred embodiment thereof made with reference to the accompanying drawings, of which:

Fig. 1 is a schematic plan view of a loading section of a semiconductor manufacturing apparatus;

Fig. 2 is a perspective view of a conventional load port of the semiconductor manufacturing apparatus;

Fig. 3 is a perspective view of a load port of a semiconductor manufacturing apparatus according to the present invention;

Fig. 4 is a sectional view of the load port of Fig. 3 taken along line I-I of Fig. 3; and

Fig. 5 is a cross-sectional view of the load port of FIG. 3 in a state in which a FOUP has been properly placed thereon.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

[0090] The present invention will now be described in more detail referring to Figs. 3 and 4.

[0095] The load port of the present invention is located outside of the mini-chamber 110 (see Fig. 1) equipped with a wafer transfer robot (112) and

a door opening robot. The load port includes a support member 300 having a flat upper surface, a plurality of kinematic coupling pins 320 protruding from the upper flat surface of the support member 300, and a plurality of sensors 340. A respective sensor 340 protrudes upwardly from within each of the kinematic coupling pins 320. Preferably, at least three kinematic coupling pins 320 and the sensors 340 are provided so that the FOUP 130 can be balanced thereon in a state in which the sensors 340 reliably contact the bottom of the FOUP 130.

[0100] According to the present invention, the kinematic coupling pins 320 and the sensors 340 are integrated. The contacts of the sensors 340, except at upper end portions thereof, are embedded in the kinematic coupling pins 320, respectively. That is, the upper end portion of each sensor 340 is exposed.

[0105] Fig. 5 shows a FOUP that has been properly placed on the load port. The FOUP 130 includes grooves 132 that receive the kinematic coupling pins 320 and the sensors 340 when the FOUP 130 rests on the load port. The sensors 340 can sense whether the FOUP 130 is properly situated on the load port. In addition, the sensors 340 may also be of a type that can discriminate a FOUP 130 loaded with wafers from an empty FOUP 130.

[0110] To this end, the sensors 340 may comprise at least one weight-detecting sensor such as a piezoelectric sensor. For example, if three

piezoelectric weight-detecting sensors 340 are used, and the sensors 340 detect different weights, the FOUP 130 is determined as not resting properly on the load port. In addition, the weight of the FOUP 130, as detected by the sensors 340, can be compared to a predetermined value to discriminate whether any wafers 140 are present in the FOUP 130. Furthermore, the number of wafers 140 stacked in the FOUP 130 can be determined from the known value of the weight of one wafer. A more accurate and efficient semiconductor manufacturing process can thus be carried out by making the above-described determinations in advance.

[0115] Alternatively, the sensors 340 may comprise photo sensors or on-off switches. In these cases, the sensors 340 can only sense whether the FOUP is properly resting on the load port. In any case, the present invention is not limited to any particular type of sensor 340. Any type of sensor that can be integrated with the kinematic coupling pin and can at least detect whether the FOUP is resting thereon may be used.

[0120] The operation of the loading section of a semiconductor manufacturing apparatus, comprising a load port according to the present invention, will now be described in detail.

[0130] The FOUP 130 is transferred to the support member 300 of the load port by an operator or an AMHS. Subsequently, the sensors 340 detect whether the FOUP 130 has been placed properly on the load port by sensing

for the presence of the bottom of the FOUP 130 at each site of the integrated pin/sensor or by sensing whether the weight (load) of the FOUP 130 is equally distributed among the sensors 340. The sensors 340 may also detect whether any wafers are stored in the FOUP 130 by measuring the load exerted by the FOUP 130 at the sites of the integrated pins/sensors and comparing the total load to a predetermined value corresponding to the weight of the FOUP 130. Likewise, the number of wafers in the FOUP 130 can be determined if the weight of each wafer is known.

[0135] A control signal is issued once the cassette is determined to be situated properly at the load port (balanced on the pins 320), and is determined to contain wafers. As a result of the issuing of these command signals, the door opening robot (not shown) opens a front door of the FOUP 130, and the wafer transfer robot (112 in Fig. 1) transfers wafers 140 from the FOUP 130 into the load-lock chamber (120 in Fig. 1). The wafers 140 transferred to the load lock chamber 120 are transferred then to another chamber, e.g., a process chamber, by another robot.

[0140] However, if all of the sensors 340 do not contact the bottom surface of the FOUP 130 sufficiently, the FOUP 130 is determined as not having been properly placed on the load port. Accordingly, the semiconductor manufacturing process does not proceed until the FOUP 130 is properly placed on the load port 300. Also, if the FOUP 130 does not contain any

wafers even though is sensed as resting properly on the support member 300 of the load port, a semiconductor manufacturing process does not proceed.

[0145] As described above, whether a wafer cassette has been properly placed on the load port and/or whether the wafer cassette contains wafers can be accurately determined by the present invention because the kinematic coupling pins and the sensors are integral. Accordingly, the present invention enhances the reliability of the semiconductor manufacturing process.

[0150] Finally, although the present invention has been shown and described with reference to the preferred embodiments thereof, various changes in form and details, as will become apparent to those of ordinary skill in the art, may be made thereto without departing from the true spirit and scope of the invention as defined by the appended claims.